DS-02-021

February 5, 2004

To: Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject: | Serial No. 10/615,123 07/08/03 |

Rainer Krenzke

SLEW RATE SENSING AND CONTROL OF A HIGH-VOLTAGE OUTPUT DRIVER FOR A VARIABLE VOLTAGE RANGE AND VARIABLE OUTPUT LOAD

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 17 , 2004.

Stephen B. Ackerman, Reg.# 37761

Signature/Date Stephen B. Ackerman 2/17/04

U.S. Patent Application Publication US 2002/0063590 A1 to Nanba et al., "Low Power Circuit with Proper Slew Rate by Automatic Adjustment of Bias Current," discusses a semi-conductor integrated circuit including an adjusted circuit a slew rate (an amount of change in output voltage per unit time for step input) of whose output is dependent on a bias current and a circuit automatically adjusting the value of the bias current to achieve low power with proper slew rate.

U.S. Patent 6,288,563 to Muljono et al., "Slew Rate Control," discusses controlling the slew rate of a driver circuit.

International Patent Applicatiaon WO 99/10982 to Garrett et al., "Current Control Technique," discloses an output driver circuit and current control technique to facilitate high-speed buses with low noise used to interface with high-speed dynamic RAMs (DRAMs).

U.S. Patent 6,009,487 to Davis et al., "Method and Apparatus for Setting a Current of an Output Driver for the High Speed Bus," discloses a method for setting a current of a current source driver for a high speed bus system.

U.S. Patent 5,254,883 to Horowitz et al., "Electrical Current Source Circuitry for a Bus," describes electrical current source circuitry for a bus.

Sincerely,

Stephen B. Ackerman,
Reg. No. 37761

Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
DS-02-021	10/615,123
Applicant: Rainer Krencke	
Filing Date: 07/08/03	Group Art Unit

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6288563	9/11/01	Muljono et al.	326	27	12/31/98
	6009487	12/28/99	Davis et al.	710	105	5/31/96
	5254883	10/19/93	Horowitz et al.	307	443	4/22/92

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO
	WO 99/10982	3/4/99	Int'l Patent App.	H03K	19/0175		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

—	U.S. Patent App. Pub. US 2002/0063590 A1 to Nanba et al., Pub. Date 5/30/02, U.S. Class 327/170, Filed 8/6/01.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.